ASSESSMENT RUBRICS LAB # 01

Introduction to Basic Electrical Equipments

| **LAB REPORT ASSESSMENT** | | | | |
| --- | --- | --- | --- | --- |
| **Criteria** | **Excellent** | **Average** | **Nil** | **Marks Obtained** |
| 1. **Objectives of Lab** | All objectives of lab are properly covered  [Marks 1] | Objectives of lab are partially covered  [Marks 0.5] | Objectives of lab are not shown  [Marks 0] |  |
| 1. **Resistance, Voltage & Current** | Correct resistance, current and voltage statements and mathematical expressions are written. Circuit diagram shown is correct and properly labeled  [Marks 2] | Correct resistance, current and voltage statements or mathematical expression or circuit diagram is missing or circuit diagram is not properly labeled  [Marks 1] | Resistance, current and voltage statements, mathematical expression or circuit diagram are incorrect or missing.  [Marks 0] |  |
| 1. **Digital Multimeter** | Properly defined DMM and explained functionality in terms of voltage, current and resistance. Explain all steps required to calculate (voltage, current and resistance) measurement. Properly labeled DMM diagram is shown.  [Marks 2] | DMM and its functionality in terms of voltage, current and resistance are not properly explained. Steps required to calculate (voltage, current and resistance) measurement are partially shown. DMM diagram is shown but not labeled.  [Marks 1] | DMM and its functionality in terms of voltage, current and resistance are not explained. Steps required to calculate (voltage, current and resistance) measurement are not shown. DMM diagram is not shown  [Marks 0] |  |
| 1. **Power Supply** | Power supply is properly defined. Functionality and steps to provide source voltage to circuit are shown. Diagram is shown with all labels and available voltage values.  [Marks 1] | Power supply is not well defined. Functionality and steps to provide source voltage to circuit are not properly shown. Diagram is shown with no labels.  [Marks 0.5] | No steps for functionality of power supply are shown  [Marks 0] |  |
| 1. **Bread Board** | Breadboard is properly defined. Functionality and steps to design series parallel circuit are shown.  Open and short circuits are also defined. Diagrams are shown with all labels. [Marks 2] | Breadboard is partially defined. Functionality and steps to design series parallel circuit are not shown.  Information about open and short circuits are unsatisfactory.  Diagrams are shown with no labels. [Marks 1] | No steps for breadboard functionality are shown.  [Marks 0] |  |
| 1. **Observations & Calculations** | All experimental results are completely shown in form of table for varying voltages and resistances.  [Marks 2] | Experimental results are partially shown and some of the observations are missing  [Marks 1] | No experimental results are shown  [Marks 0] |  |
| Total Marks Obtained:\_\_\_\_\_\_\_\_\_\_    Instructor Signature: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ | | | | |
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